

## REMARKS

Claims 1-70, 74, 75, 77 and 80 2-88 are canceled; claims 71 and 72 are amended; and claims 71-73, 76 and 78-81 are pending in the application.

As a preliminary matter, the Examiner indicates that several documents submitted with applicant's Information Disclosure Statement were not sufficiently legible. Submitted herewith is another Information Disclosure Statement resubmitting such documents, with the documents being included with the Information Disclosure statement in appropriate legible form.

The Examiner objects to claims 71 and 77 for various informalities. Applicant has removed the language that the Examiner objected to from claim 71, and has canceled claim 77. Applicant therefore requests withdrawal of the Examiner's objections to claims 71 and 77.

The pending claims stand rejected over Fitzgerald, Chu and Meyerson. Applicant has amended independent claim 71, from which the remaining claims depend, and believes that such amendments place all of the claims in condition for allowance.

Amended claim 71 recites a computer system comprising the an inverter which includes a structure comprising silicon and germanium; a PFET supported by the structure; a first layer of semiconductive material over the PFET; a second layer of semiconductive material over the first layer, with the second layer being compositionally different from the first layer; and an NFET supported by the first and second layers of semiconductive material, with the NFET having a gate which is directly over a gate of the PFET.

The amendments to claim 71 are supported by the originally-filed specification at, for example, Fig. 11 and the text describing Fig. 11, and therefore do not comprise "matter".

Specifically, an exemplary recited structure comprising silicon and germanium is shown as structure 110 of Fig. 11; an exemplary recited PFET is shown in Fig. 11 as transistor 102; an exemplary recited first layer of semiconductor material is shown as layer 16; an exemplary recited second layer of semiconductive material is shown as layer 26; and an exemplary recited NFET device is shown as device 50.

Amended claim 71 is allowable over the cited references for at least the reason that the references do not suggest or disclose all of the recited features of such claim. For instance, there is no teaching amongst the references for the recited construction having two layers of semiconductor material that are compositionally different from one another, and which are over a PFET and supporting an NFET.


Claims 72, 73, 76 and 78-81 depend from claim 71, and are therefore allowable for at least the reasons for which claim 71 is allowable.

Claims 71-73, 76 and 78-81 are allowable over the cited references for the reasons discussed above, and applicant therefore requests formal allowance of such claims in the Examiner's next action.

Dated: 11/28/08

*Encls. IDS and cited references*

Respectfully submitted

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